

FIG. 1 (PRIOR ART)

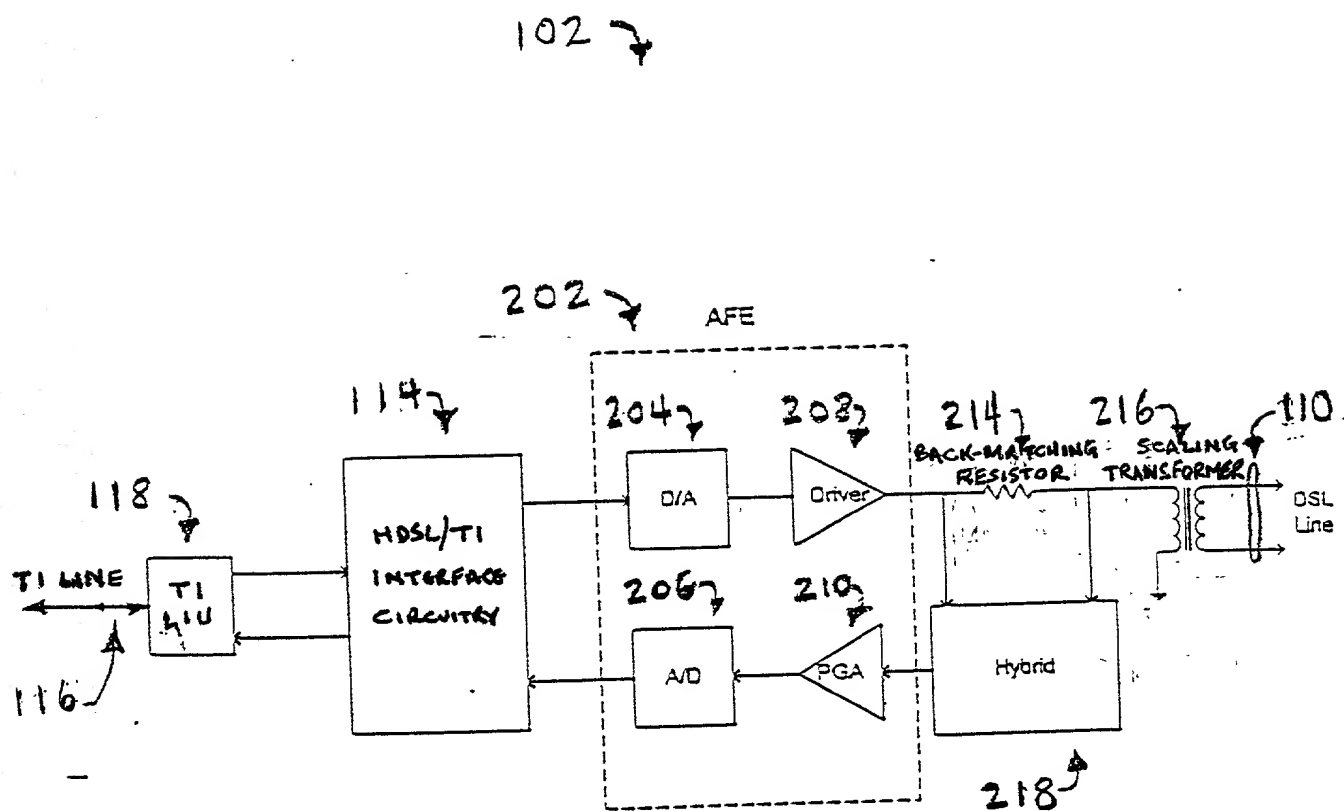


FIG. 2 (PRIOR ART)

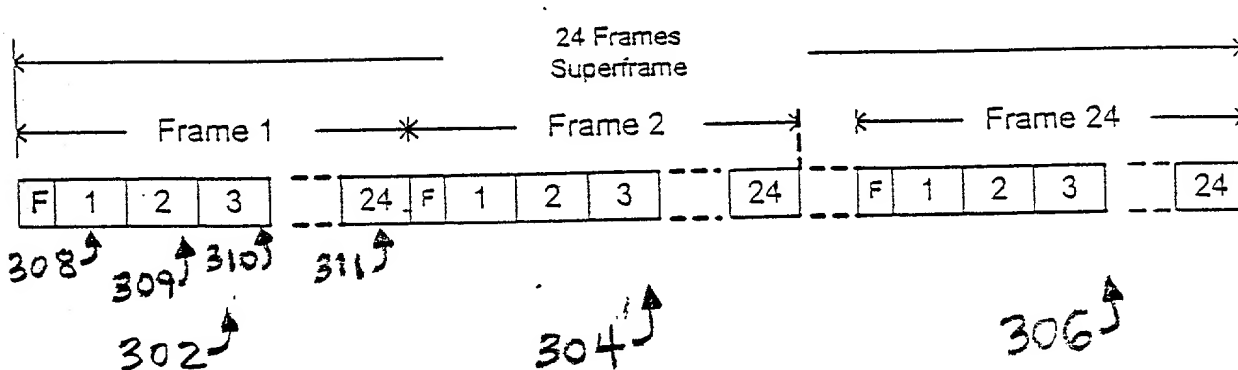


FIG. 3 (PRIOR ART)

400

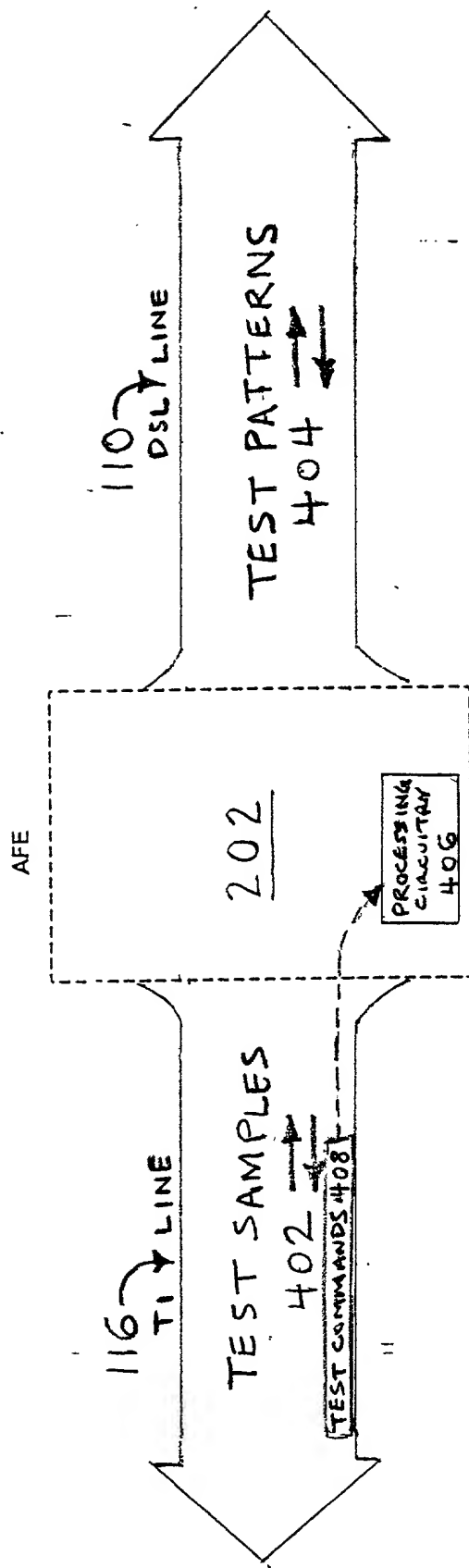


FIG. 4

500 ↘

502 ↘

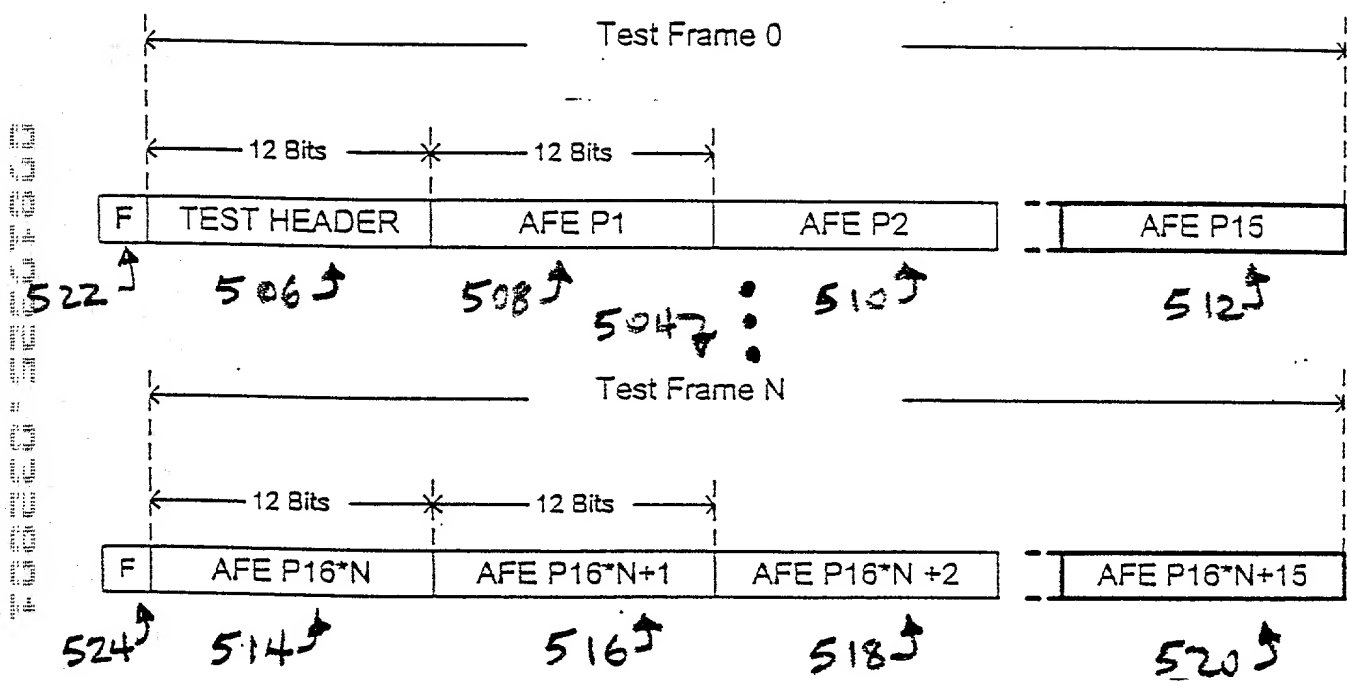


FIG. 5

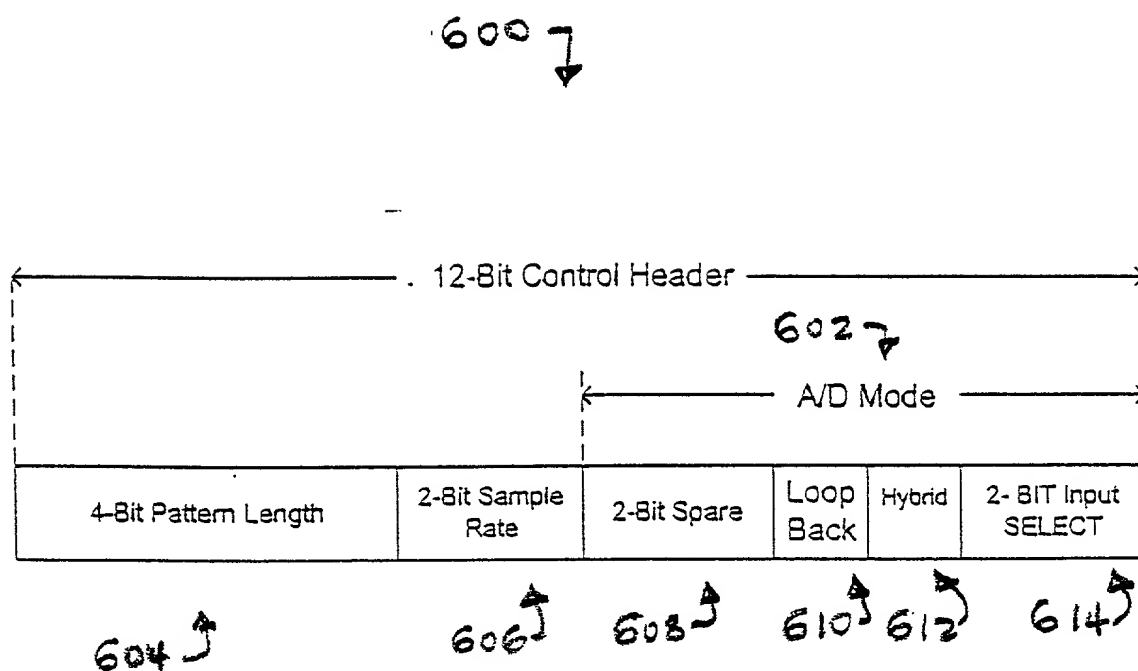


FIG. 6

700 ↘

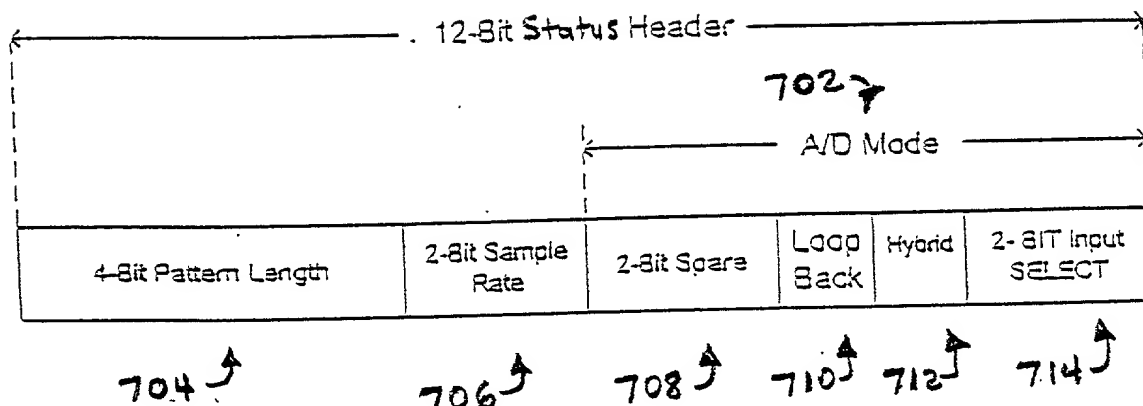


FIG. 7

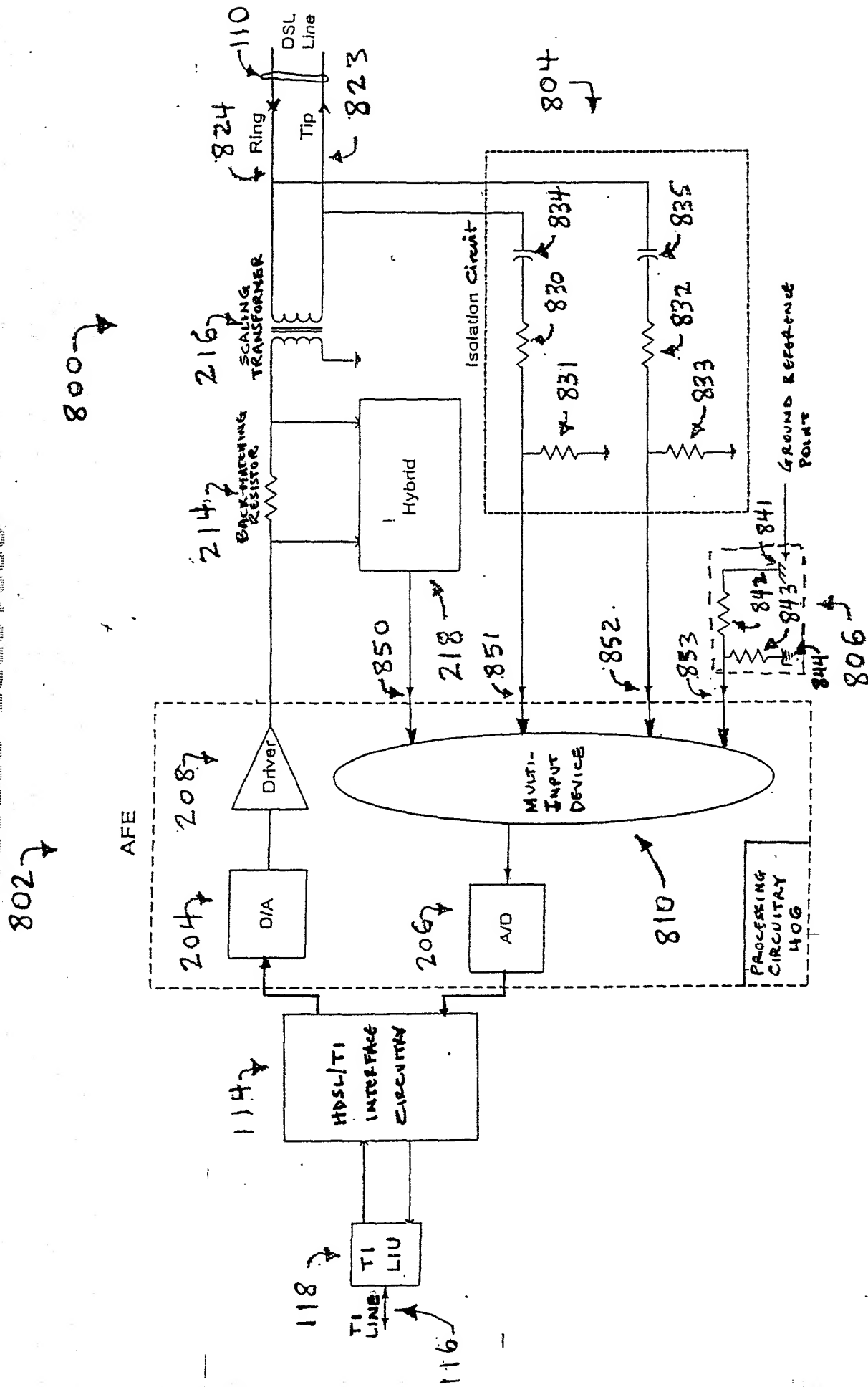


FIG. 8



FIG. 9 is a flowchart illustrating a process for testing a DSL line via an AFE. The process begins at a START block (902), followed by transmitting test samples to the AFE (904). The AFE then interprets these samples as digitized values (906). Subsequently, the AFE generates test patterns from the digitized values (908). Finally, the DSL line is tested via the AFE using these test patterns (910), and the process ends at a STOP block (912).

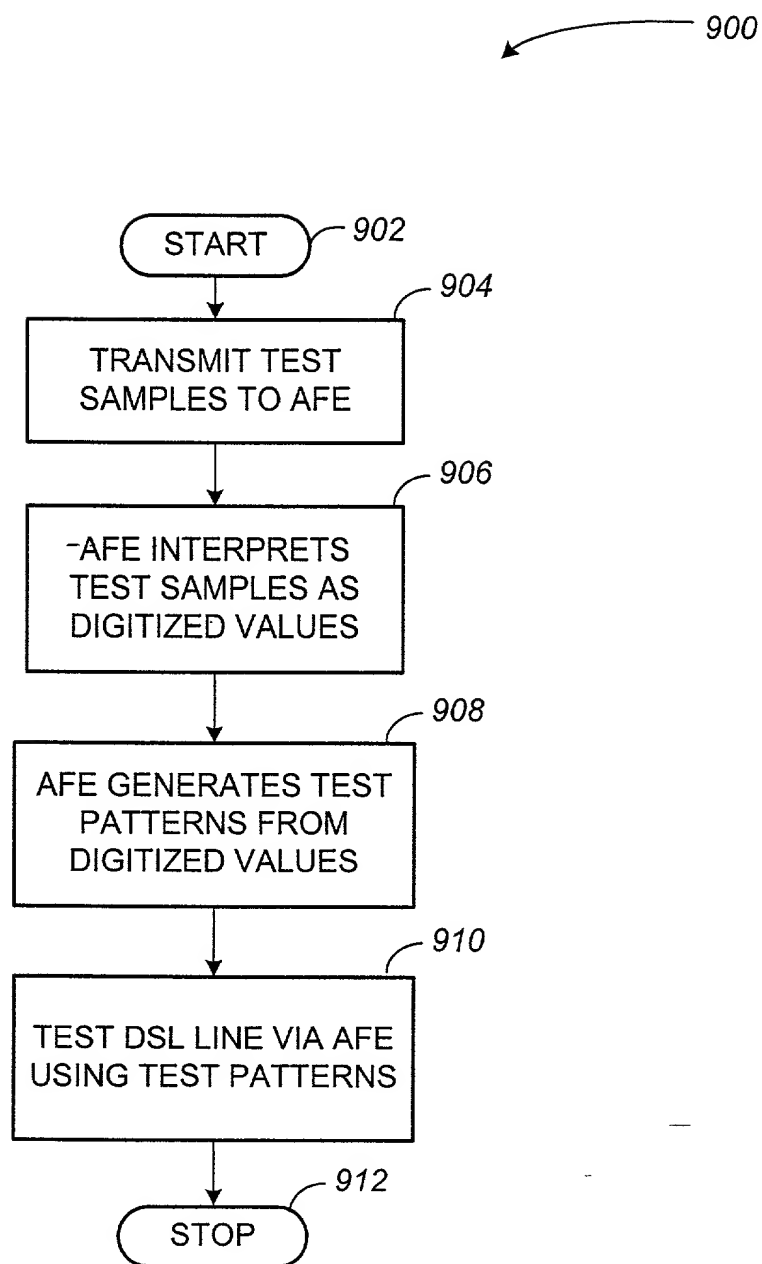


FIG. 9

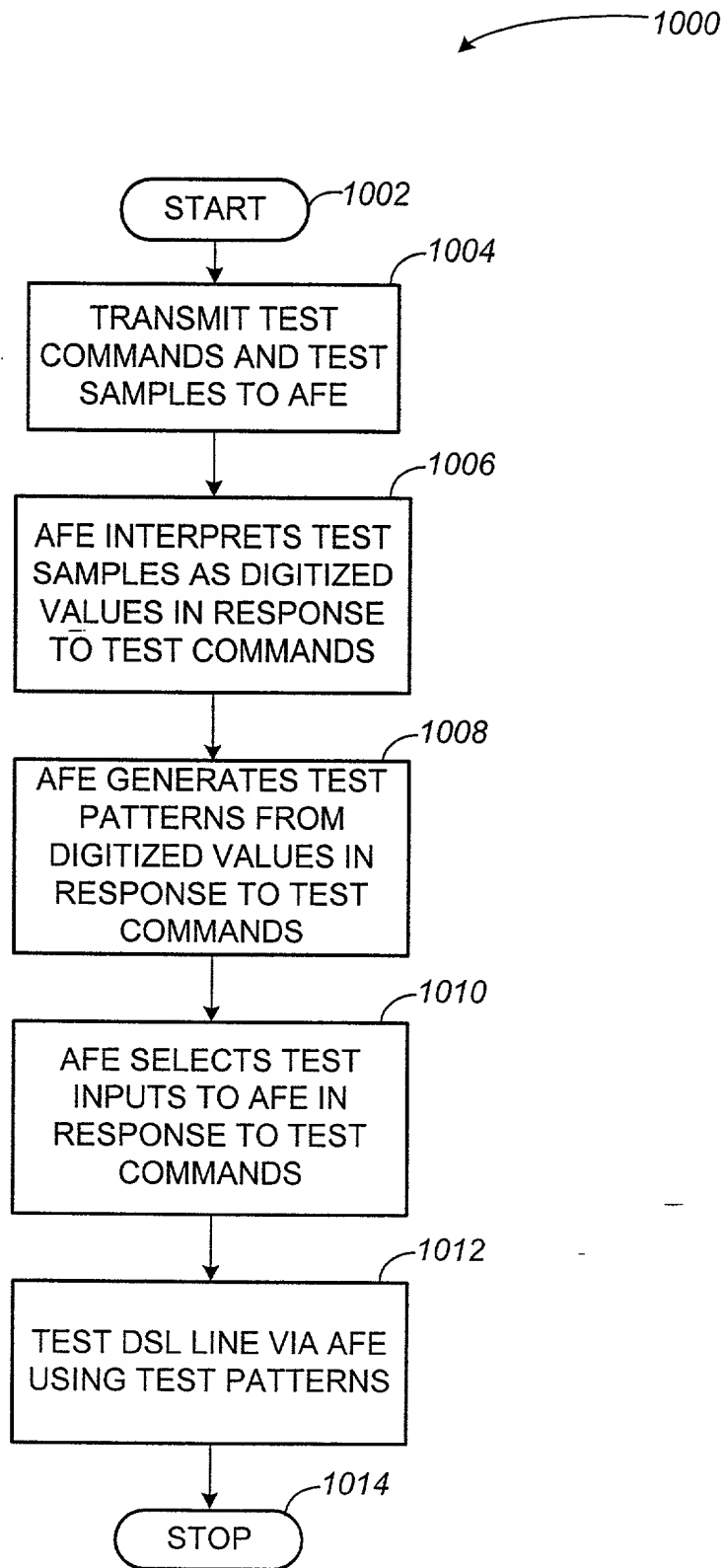


FIG. 10